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NOTICE OF ALLOWANCE AND FEE(S) DUE

34610

7590

05/09/2008

KED & ASSOCIATES, LLP P.O. Box 221200 Chantilly, VA 20153-1200 EXAMINER

PHAN, TRONG Q

ART UNIT PAPER NUMBER

2827

DATE MAILED: 05/09/2008

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/812.894	03/31/2004	Stephen H. Tang	INTEL-0056	4982

TITLE OF INVENTION: SRAM DEVICE HAVING FORWARD BODY BIAS CONTROL

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1440	\$300	\$0	\$1740	08/11/2008

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

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INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where n

appropriate. All further correspondence including the Patent, advance ordered indicated unless corrected below or directed otherwise in Block 1, by (a) maintenance fee notifications. CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address) 34610 7590 05/09/2008] H	Note Fee(s	: A certificate of ma s) Transmittal. This c rs. Each additional pa	iling can only be used for	or domestic mailings of the for any other accompanying ent or formal drawing, must
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KED & ASSO P.O. Box 221200 Chantilly, VA 20] S £	l here State addre trans	eby certify that this I is Postal Service with essed to the Mail St	ee(s) Transmittal is being sufficient postage for fir op ISSUE FEE address (571) 273-2885, on the d	g deposited with the United st class mail in an envelope above, or being facsimile	
							(Depositor's name)
							(Signature)
							(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENT	OR	A	TTORNEY DOCKET NO.	CONFIRMATION NO.
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nonprovisional	NO	\$1440	\$300		\$0	\$1740	08/11/2008
EXAM	INER	ART UNIT	CLASS-SUBCLASS				
PHAN, T	RONG Q	2827	365-154000				
 Change of correspondence address or indication of "Fee Address" (37 CFR 1.363). Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached. "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Custome Number is required. 			or agents OR, alteri (2) the name of a si registered attorney 2 registered patent listed, no name will	the names of up to 3 registered patent attorneys agents OR, alternatively, the name of a single firm (having as a member a gistered attorney or agent) and the names of up to registered patent attorneys or agents. If no name is sted, no name will be printed.			
recordation as set fortl (A) NAME OF ASSIG	less an assignee is identi h in 37 CFR 3.11. Comp GNEE	ified below, no assignee oletion of this form is NC	e data will appear on th DT a substitute for filing (B) RESIDENCE: (C.	e pa an a	tent. If an assignee assignment. and STATE OR COU	JNTRY)	ocument has been filed for
Please check the appropri	iate assignee category or	categories (will not be p	erinted on the patent):		Individual 🖵 Corpo	oration or other private gr	oup entity Government
4a. The following fee(s) are submitted: ☐ Issue Fee ☐ Publication Fee (No small entity discount permitted) ☐ Advance Order - # of Copies			 4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above) A check is enclosed. Payment by credit card. Form PTO-2038 is attached. The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number (enclose an extra copy of this form). 				
**	s SMALL ENTITY statu	is. See 37 CFR 1.27.	==		=	ENTITY status. See 37 C	
interest as shown by the	d Publication Fee (if requestion fee the United Sta	tes Patent and Trademarl	k Office.	an tn	e applicant; a register	ed attorney or agent; or ti	ne assignee or other party in
Authorized Signature			Date				
Typed or printed name			Registration No ation is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process)				
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KED & ASSOCI	IATES, LLP	PHAN, TRONG Q			
P.O. Box 221200			ART UNIT	PAPER NUMBER	
Chantilly, VA 20153-1200			2827		
			DATE MAILED: 05/09/200	8	

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 0 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 0 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

	Application No.	Applicant(s)
	Application No.	Applicant(s)
Notice of Allowability	10/812,894	TANG ET AL.
Notice of Allowability	Examiner	Art Unit
	TRONG PHAN	2827
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI	(OR REMAINS) CLOSED in this ap or other appropriate communication IGHTS. This application is subject t	plication. If not included n will be mailed in due course. THIS
1. This communication is responsive to 2/19/08.		
2. X The allowed claim(s) is/are <u>1-3,7-11,13,15-20,24-26,29,30</u>	and 33-37.	
 3. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority do 	e been received. e been received in Application No	
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be submin INFORMAL PATENT APPLICATION (PTO-152) which give	IENT of this application. itted. Note the attached EXAMINER	R'S AMENDMENT or NOTICE OF
5. CORRECTED DRAWINGS (as "replacement sheets") mus	` , •	
 (a) ☐ including changes required by the Notice of Draftspers 		-948) attached
	- ·	-540) attached
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1	s Amendment / Comment or in the 0	
each sheet. Replacement sheet(s) should be labeled as such in t		
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT		
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	5. ☐ Notice of Informal F	(PTO-413),
3. ☐ Information Disclosure Statements (PTO/SB/08),	Paper No./Mail Da 7.	ne ment/Comment
Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	9.	ent of Reasons for Allowance
	/TRONG PHAN/ Primary Examiner, Art Uni	t 2827

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DETAILED ACTION

REASONS FOR ALLOWANCE

1. The following is an examiner's statement of reasons for allowance:

"A static random access memory (SRAM) device comprising: a first transistor pair to couple between a supply voltage line and GROUND; a second transistor pair to couple between the supply voltage line and GROUND, the supply voltage line to receive a first supply voltage when the SRAM memory device is in an ACTIVE mode and to receive a second supply voltage when the SRAM memory device is in a STANDBY mode memory, the second supply voltage being different than the first supply voltage; a first access transistor to couple to a word line, a first bit line and a common node of the second transistor pair; a second access transistor to couple to the word line, a second bit line and a common node of the first transistor pair; and a bias transistor to couple to a body of one of the transistors of the first transistor pair and to a body of one of the transistors of the second transistor pair, the bias transistor to apply a forward body bias to the one transistor of the first transistor pair and to the one transistor of the second transistor pair when the SRAM memory device is in the STANDBY mode, wherein a gate of the bias transistor is coupled to a signal line to receive a STANDBY signal indicative of the STANDBY mode of the SRAM memory device" recited in claims 1-3, and 7-8 is patentable.

"A static random access memory (SRAM) device comprising: a first SRAM memory cell having a cross-coupled inverter configuration, the cross-coupled inverter configuration including at least four transistors; a supply voltage line to

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provide a first supply voltage to two transistors of the at least four transistors of the first SRAM memory cell when the first SRAM memory cell is in an ACTIVE mode and to provide a second supply voltage to the two transistors when the first SRAM memory cell is in a STANDBY mode, the second supply voltage being different than the first supply voltage; and a switching device to apply a forward body bias to the two transistors of the cross-coupled inverter configuration of the first SRAM memory cell when the first SRAM memory cell is in the STANDBY mode, wherein the switching device comprises an NMOS transistor having a source coupled to GROUND and a gate coupled to a power control unit and wherein a gate of the NMOS transistor to receive a STANDBY signal from the power control unit indicative of the STANDBY mode of the first SRAM memory cell" recited in claims 9-11, 13 and 15-17 is patentable.

"An electronic system comprising: a processor device to process data; a static random access memory (SRAM) device to store the data; and a power control unit to control a supply voltage level applied to the SRAM device and to provide a signal indicative of a mode of the SRAM device, the power control unit to apply a first voltage level when the signal indicates that the SRAM device is in an ACTIVE mode and to apply a second voltage level when the signal indicates that the SRAM device is in a STANDBY mode, the SRAM device including: a switching device to apply a forward bias to transistors within the SRAM device when the signal provided by the power control unit indicates the STANDBY mode of the SRAM device" recited in claims 18-20, 24-26, 29-30 and 33-37 is patentable.

De et al., 6,593,799, does not teach the feature "the bias transistor to apply a forward body bias to the one transistor of the first transistor pair and to the one transistor of the second transistor pair when the SRAM memory device is in the STANDBY mode, wherein a gate of the bias transistor is coupled to a signal line to receive a STANDBY signal indicative of the STANDBY mode of the SRAM memory device". Fig. 7 of De et al., 6,593,799, is just a NAND gate circuit.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to TRONG PHAN whose telephone number is (571) 272-1794. The examiner can normally be reached on M-F (8:30-5:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, AMIR ZARABIAN can be reached on (571) 272-1852. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/TRONG PHAN/ Primary Examiner, Art Unit 2827